



Agnel Charities

**Fr. C. Rodrigues Institute of Technology, Vashi**  
(An Autonomous Institute & Permanently Affiliated to University of Mumbai)

**MID SEMESTER EXAMINATION TIME TABLE**  
**B. Tech (Semester - I, DSE- III) Second Half of 2026**

|                  |                     | SEMESTER - I                                         |                                  |                                 |                                      |                                                      |
|------------------|---------------------|------------------------------------------------------|----------------------------------|---------------------------------|--------------------------------------|------------------------------------------------------|
| Dates            | Time                | Subjects                                             |                                  |                                 |                                      |                                                      |
| Mon, 02 Nov 2026 | 10:00 AM - 11:30 AM | Engineering Mathematics-I                            |                                  |                                 |                                      |                                                      |
| Tue, 03 Nov 2026 | 10:00 AM - 11:00 AM | Engineering Physics-I                                |                                  |                                 |                                      |                                                      |
| Wed, 04 Nov 2026 | 10:00 AM - 11:00 AM | Engineering Chemistry-I                              |                                  |                                 |                                      |                                                      |
| Thu, 05 Nov 2026 | 10:00 AM - 11:30 AM | Engineering Mechanics                                |                                  |                                 |                                      |                                                      |
| Fri, 06 Nov 2026 | 10:00 AM - 11:00 AM | Basic Electrical Engineering                         |                                  |                                 |                                      |                                                      |
|                  |                     | SEMESTER - III (DSE)                                 |                                  |                                 |                                      |                                                      |
| Dates            | Time                | Computer                                             | Mechanical                       | EXTC                            | Electrical                           | CSE                                                  |
| Mon, 02 Nov 2026 | 10:00 AM - 11:30 AM | Engineering Mathematics - III                        | Engineering Mathematics-III      | Engineering Mathematics-III     | Engineering Mathematics-III          | Engineering Mathematics-III                          |
| Tue, 03 Nov 2026 | 10:00 AM - 11:30 AM | Discrete Structures and Graph Theory                 | Mechanics of Solids              | Network Theory                  | Circuit and Signal Analysis          | Discrete Structures                                  |
| Wed, 04 Nov 2026 | 10:00 AM - 11:30 AM | Data Structures                                      | Material Science and Engineering | Electronic Devices and Circuits | Elements of Power System             | Data Structures                                      |
| Thu, 05 Nov 2026 | 10:00 AM - 11:30 AM | Database Management System                           | Thermodynamics                   | Digital Circuit Design          | Renewable Sources and Energy Storage | Database Management System                           |
| Fri, 06 Nov 2026 | 10:00 AM - 11:30 AM | Digital Logic and Computer Organization Architecture | Industrial Electronics           | Data Structures and Algorithms  | Electronic Components and Circuits   | Digital Logic and Computer Organization Architecture |

Date: 22 September 2026



Dr. Sanjay Rukhande  
**Controller of Examinations**